SEMICONDUCTORS & THE WORLD TRADE ORGANIZATION

How Global Trade Rules Have Spurred Semiconductor Growth & Innovation

NOVEMBER 2020
FREE & OPEN ACCESS TO GLOBAL MARKETS HAS BEEN CRITICAL TO SEMICONDUCTOR INDUSTRY SUCCESS & TECHNOLOGY ADVANCEMENT

The steady opening of markets and leveling of the global playing field spearheaded by the World Trade Organization (WTO) over the past 25 years has been critical to the success of the global semiconductor industry. Semiconductors – the brains of all modern electronics – are a highly traded product with an incredibly complex production process and supplier ecosystem that spans many countries. In 2018, more than one trillion semiconductors were sold worldwide, accounting for $1.8 trillion in total global trade.\(^1\) Given the sheer volume and complexity of global semiconductor trade, along with high capital costs and short product life-cycles, the ability to move semiconductor goods and materials freely, fairly, and efficiently across borders has been critical to the industry’s success and technological progress. Over the past 25 years, the WTO has helped make this progress possible by opening markets and implementing uniform rules of trade. Landmark WTO agreements like the Information Technology Agreement (ITA) and ITA Expansion, Trade-Related Aspects of Intellectual Property Rights (TRIPS), and the Trade Facilitation Agreement (TFA) have drastically reduced the cost of trade, lowered consumer prices, and expanded access to productivity-enhancing tech products to people around the world. Global sales and the increasingly free flow of goods, ideas, materials, and people have in turn spurred technological progress and innovation in a virtuous circle of innovation.

This paper examines how WTO agreements related to intellectual property protection, reciprocal tariff elimination, disciplines on trade-distorting subsidies, and trade facilitation have benefited the semiconductor industry over the past 25 years. It also examines how further reforms and stronger trade rules that remove market barriers, promote fair competition, and protect IP will enhance those benefits, not just for the semiconductor industry, but for the broader global economy.

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1 UN Comtrade 2018 Trade Data
2 SIA Factbook (2020)
I. TRIPS LAID THE FOUNDATION OF IP PROTECTION FOR GLOBAL SEMICONDUCTOR INNOVATORS

The 1995 WTO Agreement on Trade-Related Aspects of Intellectual Property Rights (TRIPS) is the world’s first international trade agreement to both define multilateral IP rules and standards and mandate minimum national IP enforcement procedures within a single framework. Crucially, it made intellectual property protection subject to WTO dispute settlement and potential trade sanctions in cases of noncompliance. The global legal framework it established for protection of intellectual property has helped allow innovative firms in IP-intensive industries, including the semiconductor industry, to establish new business models centered around licensing and contract manufacturing, and also maintain a competitive edge in research and development. In short, the TRIPS Agreement was a major win for countries at the forefront of developing valuable intellectual property, as well as for consumers around the world who benefit from the end-products of these technological advances.

Strong IP protection and enforcement are an essential foundation to continued technological progress and the future competitiveness of the global semiconductor industry, incentivizing companies and research institutions to invest in R&D and share technology without compromising their returns on investment. Semiconductor companies typically spend around one-fifth of revenue on R&D, making IP protection and enforcement of utmost importance to the U.S. semiconductor industry. In 2019, semiconductor companies in the United States invested nearly $40 billion in R&D, or 18 percent of their total revenue, one of the highest rates of any industry. Additionally, among the top 15 U.S. corporate patent recipients, eight are companies in the semiconductor industry. More generally, IP-intensive industries, such as semiconductors, are estimated to contribute to more than a third of the U.S. GDP, and around 27.9 million U.S. jobs. Such high rates of R&D investment in the semiconductor industry would not be possible without the global IP protections and enforcement standards established by TRIPS. While the TRIPS agreement covers a broad range of IP trade disciplines, there are three areas of critical importance to the semiconductor industry: 1) protection for trade secrets; 2) express protections for IC layout designs; and 3) safeguards against compulsory licensing for semiconductors.

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2 Uruguay Round Agreements. WTO Legal Texts. (Available at: https://www.wto.org/english/docs_e/legal_e/legal_e.htm#TRIPs)
A. TRIPS IS THE FIRST INTERNATIONAL TRADE AGREEMENT TO PROVIDE PROTECTION FOR TRADE SECRETS

TRIPS is the first multilateral trade agreement to expressly recognize trade secrets as a form of IP and obliges members to provide a means for protecting information that is secret. This is extremely important to the semiconductor industry, for which trade secrets are a critical and major business asset. Some studies estimate for highly innovative and knowledge-intensive industries like the semiconductor industry, trade secrets can comprise up to 80 percent of the value of a company’s IP portfolio. Semiconductor trade secrets include manufacturing processes and techniques, chemical formulations, circuit designs, software source code, business strategies and customer lists. This form of IP is often more valuable to a semiconductor company than patented legal protections due to the short life-cycle of products owing to the rapid pace of technological development and upgrades. Ultimately, protecting unregistered trade secrets is critical to companies’ business models because patented legal protections quickly become obsolete due to rapid advances in semiconductor technologies. The value of the trade secret protections under TRIPS is all the more important for small and medium sized enterprises (SMEs) and start-ups that often cannot afford teams of patent attorneys. Unfortunately, while a company’s trade secrets are frequently its most valuable assets, they are also the most vulnerable. The entire economic value of a trade secret stems from the competitive advantage conferred by the confidential nature of the information. If a company’s trade secrets are stolen, it could lose its competitive advantage and its market share may be jeopardized.

While TRIPS is important in establishing WTO Members’ minimum obligations for protecting trade secrets, more needs to be done to protect this valuable form of IP, which is facing increasing and rampant misappropriation by sophisticated bad actors, including through cyber means.

B. TRIPS PROVIDES UNIQUE LEGAL RIGHTS FOR IC LAYOUT DESIGNS

Article 35 of the TRIPS Agreement provides unique IP protection for the layout designs of integrated circuits (ICs). A semiconductor layout design (also referred to as a “mask work”) is the digital topography of the transistors and other circuitry elements on a chip. Prior to 1984, these designs fell through the cracks of existing copyright and patent regimes, and it was not necessarily illegal for a company to produce a competing chip with an identical layout to its competitor’s chip. This led to concerns over “chip piracy,” in which a company could copy, for example, a chip design for $10,000 that had cost its original manufacturer $100,000 by simply taking negatives of the mask design. The United States was the first nation to pass a law protecting semiconductor layout designs with the Semiconductor Chip Protection Act (1984). Other countries soon followed suit, and in 1989 the “Washington Treaty on Intellectual Property in Respect of Integrated Circuits” (“Washington Treaty” or “IPIC Treaty”) was

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6 TRIPS Article 39 calls on members to provide for the protection of "undisclosed information" that is secret and has commercial value, and to protect such information from disclosure, acquisition or use in a manner contrary to "honest commercial practices."
9 Standards concerning the availability, scope and use of Intellectual Property Rights. WTO. (Available at: https://www.wto.org/english/docs_e/legal_e/27-trips_04c_e.htm)
signed, but never ratified. It wasn’t until the TRIPS Agreement that protection for chip layout designs was afforded on a broader global scale.

This protection became increasingly important through the 1990s and 2000s when improvements in automated design tools enabled semiconductor layout designs to be easily copied from protected layout designs. While TRIPS includes a specific exception for copies made in the course of reverse engineering, the implicit assumption for this exception was that reverse engineering would require intellectual effort. In 2004 and 2006, the World Semiconductor Council (WSC) – an international forum comprised of the semiconductor industries in China, Chinese Taipei, EU, Japan, Korea, and the U.S. – advocated that courts and IP policymakers carefully review the existence of intellectual effort, the use of improved automated design tools, and similarity (rather than identicality) to an original protected layout design when adjudicating infringement claims under TRIPS and national layout design protection laws.

While IC layout protection was very important to the industry for several decades, advances in semiconductor technology make trade secrets a more valuable and practical route of IP protection today. Thus, there is a strong need to develop new and stronger multilateral rules to strengthen trade secret protection.

C. TRIPS SAFEGUARDS AGAINST COMPULSORY LICENSING OF SEMICONDUCTORS

Article 31 of the TRIPS Agreement governs “compulsory licensing,” a practice in which a government compels a patent holder to allow another entity to produce and sell its patented product or process, which is an exception to typical patent protection rights. While TRIPS allows compulsory licensing, it establishes a number of procedural and substantive safeguards designed to prevent governments from abusing compulsory licensing to enable or create more competitors, or to restrict trade. Specifically, TRIPS: 1) only allows compulsory licensing of patents on a case-by-case basis (not by rule or general guideline); 2) only allows compulsory licensing after there is an effort to obtain authorization from the patent-holder on reasonable commercial terms within a reasonable time-frame; and 3) and only in cases of national emergencies, other “circumstances of extreme urgency” or in cases of “public non-commercial use” will the requirement for negotiation with the patent holder described in section 2 be waived and the government can proceed directly to compulsory licensing.

Critically, TRIPS Article 31(c) provides further protection from compulsory licensing for semiconductor technology by limiting any compulsory licensing to “public non-commercial use.” This means semiconductor innovators are only subject to compulsory licensing in situations of use by the government itself or a government contractor (creating semiconductors solely for government use of the chips or a product only procured by the government), and thus a semiconductor company cannot be compelled to transfer valuable intellectual property to indigenous competitors.

13 Patents bestow exclusive rights on their owners to prevent others from making, selling, using or importing a product or process.
This international rule has been critical in confronting countries that are pursuing expansive compulsory licensing policies to increase their access to foreign IP in: 1) areas where access is deemed to be in the "public interest," i.e. pharmaceutical, biomedical, and environmental technologies; and 2) critical technologies owned by "dominant" companies where access by smaller competitors is allegedly needed to compete. TRIPS obligations also specifically limit compulsory licensing to very narrow circumstances, so as to incentivize successful companies to continue to invest and innovate. Without specific guidance and narrow provisions under which innovations could be subject to compulsory license, major research and development enterprises would be reluctant to make the necessary investments to develop their most valuable innovations.

II. ITA AND ITA EXPANSION HAS ACCELERATED ICT DEMAND, LOWERED CONSUMER PRICES, AND STRENGTHENED THE SEMICONDUCTOR ECOSYSTEM

Over 80 industry associations representing both high- and low-tech companies from around the world have declared the Information Technology Agreement (ITA) to be one of the most meaningful and successful trade agreements in the history of the World Trade Organization. Originally signed in 1996, the ITA and its expansion in 2015 eliminated tariffs on approximately $3 trillion of information communication technology (ICT) goods traded globally every year. The signatories of the ITA and its expansion, which account for more than 97 percent of trade in these products, agreed to eliminate tariffs on the covered products on a reciprocal basis. The 2015 expansion rendered annual global tariff savings of $13.8 billion – without accounting for increased economic activity.

The semiconductor industry is perhaps the greatest beneficiary of the ITA and its expansion. Semiconductors are the largest ITA product category, accounting for 32 percent of global trade of ITA products in 2015. The ITA expansion resolved non-uniform tariff classification of advanced semiconductors known as multi-component ICs (MCOs), which before 2015 were typically classified as parts of other equipment rather than as a semiconductor and subject to tariffs as high as 25 percent. The elimination of tariffs on MCOs alone provides roughly $150-300 million a year in tariff savings for U.S. companies. The most significant benefit to the semiconductor industry, however, is the growth in global demand for semiconductor-enabled ICT products accelerated by the pioneering trade pact. Tariff elimination "decreases the cost of innovation-and productivity-enhancing ICT capital goods, which spurs their adoption and consumption among businesses and consumers alike." Between 1996 and 2015, world exports of ICT products covered by the ITA tripled to $1.7 trillion. This is especially

15 The 1996 Agreement eliminated tariffs on $1.7 trillion of goods, and the 2015 ITA expansion covered an additional $1.3 trillion. “20 Years of the WTO,” WTO Publication, 2017
17 20 Years of the WTO. WTO, 2017. (Available at: https://www.wto.org/english/res_e/publications_e/ita20years2017_e.htm)
18 The Benefits of Including Multi-Component Semiconductors in an Expanded Information Technology Agreement. SIA, 2014. (Available at: https://www.semiconductors.org/the-benefits-of-including-multi-component-semiconductors-in-an-expanded-information-technology-agreement/)
significant considering that the price of ICT products has also declined dramatically during the same period. The U.S. Bureau of Labor and Statistics estimates that the import price level for “computers, peripherals and semiconductors” has dropped 96 percent between 1996 and 2015. The increased demand for ICT products driven by lower costs and greater technology diffusion has significantly boosted demand for semiconductors. Between 2015 and 2018, global semiconductor sales jumped from about $330 billion to $468 billion.

EXAMPLES OF PRODUCTS COVERED UNDER ITA & ITA EXPANSION

<table>
<thead>
<tr>
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<tbody>
<tr>
<td>computers and peripheral equipment</td>
<td>electronic devices (video recording, digital car radios, set top boxes)</td>
</tr>
<tr>
<td>electrical components such as semiconductors</td>
<td>video games and consoles</td>
</tr>
<tr>
<td>computer software</td>
<td>audiovisual-multimedia (GPS, DVD players, Smart cards, optical media)</td>
</tr>
<tr>
<td>telecommunications equipment</td>
<td>multifunctional printing and copying machines, ink cartridges</td>
</tr>
<tr>
<td>analytical instruments</td>
<td>multicomponent integrated circuits (MCOs) and multi-chip packages (MCPs)</td>
</tr>
</tbody>
</table>

The “zero-in, zero-out” trading environment facilitated by the ITA has been a huge boon to intermediate products like semiconductors, which have complex global production chains. A typical semiconductor crosses borders many times throughout its production cycle and before its eventual integration into an end-product. A tariff-free environment eliminates not just the tariff, but also the costly, burdensome and often time-consuming administrative customs procedures for goods crossing borders. Thus, the ITA has also greatly strengthened the semiconductor ecosystem and all the players in it, including designers, manufacturers, assembly and test operations, downstream electronics industries, and customers.

By lowering the cost of semiconductors and boosting greater ICT trade, the ITA has been a win-win-win for countries, companies, and consumers. For countries, it promotes more affordable ICT infrastructure and greater connectivity, spurs economic growth and productivity, and increases employment, investment and export opportunities. For companies, it increases efficiency and allows companies to channel funds that otherwise would have been spent on tariffs into R&D for future innovations (as mentioned above, semiconductor companies on average re-invest one-fifth of sales revenues back into R&D). For consumers, it has lowered prices of key consumer goods, and increased access to life-

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changing technologies in areas such as telecommunications, computing, energy efficiency, transportation, health care, medical technology, artificial intelligence, automation, and more.

McKinsey & Company has reported that if the automobile industry had similar improvements in price and performance to semiconductors over three decades, “a Rolls-Royce would cost only US $40 and could circle the globe eight times on one gallon of gas - with a top speed of 2.4 million miles per hour.”

The ability to access and freely move products, talent, materials, resources, and knowledge across borders has been a key driver of this extraordinary growth and cost reduction.

### 25 YEARS OF SEMICONDUCTOR INDUSTRY ADVANCEMENT  

<table>
<thead>
<tr>
<th>Semiconductor Growth Metric</th>
<th>1996</th>
<th>2019</th>
<th>% Change</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Total Semiconductor Trade</strong></td>
<td>$294 billion*</td>
<td>$1.655 trillion*</td>
<td>463%</td>
</tr>
<tr>
<td><strong>Cell Phone Subscriptions</strong></td>
<td>3.022*</td>
<td>104.9*</td>
<td>3,371%</td>
</tr>
<tr>
<td>(Per 100 People)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Internet Users</strong></td>
<td>1.19*</td>
<td>49.7*</td>
<td>4,076%</td>
</tr>
<tr>
<td>(Per 100 People)</td>
<td></td>
<td></td>
<td></td>
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<tr>
<td><strong>Global Semiconductor Market</strong></td>
<td>$132 billion</td>
<td>$412 billion</td>
<td>212%</td>
</tr>
<tr>
<td>(Total Revenue)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Semiconductor Units Produced</strong></td>
<td>215 billion</td>
<td>932 billion</td>
<td>333%</td>
</tr>
<tr>
<td>(Total Number)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Transistors Produced</strong></td>
<td>29.1 quadrillion (29.1 x 10^{14})</td>
<td>262 sextillion (2.62 x 10^{21})</td>
<td>900,343,543%</td>
</tr>
<tr>
<td>(Estimated Total Number)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Transistors per Semiconductor</strong></td>
<td>135,237</td>
<td>2,809,901,908</td>
<td>2,077,661%</td>
</tr>
<tr>
<td>(Average Number)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>PC Processor Speeds</strong></td>
<td>133 MHz</td>
<td>2,300 MHz</td>
<td>1,629%</td>
</tr>
<tr>
<td>Single Core</td>
<td></td>
<td>Quad Core</td>
<td></td>
</tr>
<tr>
<td><strong>Total Worldwide IC Wafer Capacity</strong></td>
<td>51.9 million</td>
<td>230.5 million</td>
<td>344%</td>
</tr>
<tr>
<td>(200 mm Equivalents)</td>
<td></td>
<td></td>
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<tr>
<td><strong>Worldwide Semiconductor Industry R&amp;D Spending</strong></td>
<td>$15.7 billion</td>
<td>$64.5 billion</td>
<td>311%</td>
</tr>
<tr>
<td><strong>Process Technology Node</strong></td>
<td>350 nanometers</td>
<td>7 nanometers</td>
<td>-98%</td>
</tr>
<tr>
<td>(Average Share)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Semiconductor Content per Electronic System</strong></td>
<td>18.8%</td>
<td>26.3%</td>
<td>40%</td>
</tr>
</tbody>
</table>

Note: Value amounts ($) expressed in US dollars.

III. SCM AGREEMENT WAS A SIGNIFICANT STEP TOWARD FAIR, MARKET-BASED COMPETITION AMONG COMPANIES

The Agreement on Subsidies and Countervailing Measures ("ASCM" or "SCM Agreement") was designed to address trade-distorting subsidies governments use to give their firms an unfair competitive advantage. Because of the semiconductor industry's high capital and innovation costs, government investment has been a prominent contributor to the development of semiconductor production since the early days of the industry. While market-based government support can help fuel innovation and technological diffusion, non-transparent and discriminatory subsidies can lead to unfair competitive conditions and create major market distortions. Although government subsidies continue to present major challenges in the trading system, the SCM Agreement represents an important first step toward establishing rules and enforcement tools intended to curb market-distorting subsidies so that the competitiveness of companies and their products, not government intervention, is the principal driver of industry success.

Specifically, the SCM Agreement has played an important role in defining and differentiating between appropriate and inappropriate government support for industry. The original 1995 agreement prohibited two types of blatantly discriminatory subsidies ("prohibited subsidies"): export promotion and import substitution (i.e. use of domestic products). Additionally, the agreement allowed members to take countervailing duty (CVD) actions against subsidies that cause "material injury" to their own domestic industry ("actionable subsidies"), and it established rules and procedures allowing WTO members to challenge trade-distorting domestic subsidies that cause "adverse effects." Lastly, the original agreement permitted subsidies in certain areas like R&D activity ("non-actionable subsidies"). Part VII of the SCM Agreement also obliges members to notify other members of all specific subsidies granted or maintained within their territories. This notification mechanism was intended to introduce greater transparency to government support to allow other members to evaluate the trade effects and understand the operation of subsidy programs.

The WTO rules on prohibited export and import substitution subsidies have been somewhat effective. In fact, the first WTO case filed against China involved import substitution in the semiconductor industry. In 2004, the U.S. government successfully challenged a major Chinese tax subsidy contingent on local ICT production. As a result of the WTO challenge, China agreed to cease granting a discriminatory VAT rebate amounting to 14 to 17 percentage points that was granted to only to domestic semiconductors. The U.S. government has also successfully challenged a wide array of other prohibited subsidies provided by China and other countries.

It has become apparent, however, that the SCM disciplines regarding trade-distorting domestic subsidies are limited in effect and have failed to account for different economic systems that blur the lines between public and private funding. For example, most foreign subsidies for semiconductors are

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being provided by state-owned banks or government-guided investment funds that may not possess “government authority” or perform a “government function,” but are directly or indirectly controlled by the government through minority or majority ownership interests. The current WTO rules are undermined by the lack of clear guidance on the definitions of what constitutes a “public body,” the difficulties surrounding proving “adverse effects,” the lack of rules that effectively discipline subsidies that may create excess capacity and third-country market displacement, and lack of enforcement of the transparency and notification obligations. Stronger rules are needed to avoid the harmful effects of non-market and non-transparent government support in the semiconductor sector that discriminates against foreign competitors, generates excess capacity, or distorts trade.

### IV. TFA HAS LOWERED COSTS OF SEMICONDUCTOR TRADE

Semiconductors are a highly traded product that have a complex manufacturing supply chain. Overly complicated customs and trade procedures, obligations, and practices have the potential to significantly disrupt semiconductor supply chains, creating costly impediments that impair companies and consumers. The 2017 ratification of the WTO’s Trade Facilitation Agreement (TFA) was lauded by the global semiconductor industry for lowering trade costs as goods move across borders by: 1) expediting import, export and in-country transit; 2) removing bureaucratic red tape and corruption; 3) making border processes more efficient and transparent; and 4) focusing on technological advances to achieve such objectives. The WTO estimates that removing burdensome regulations and simplifying customs processes lowers trade costs by the equivalent of a 134 percent ad valorem tariff. Critically, because 82 percent of U.S. semiconductor sales are to overseas customers

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26 Trade Facilitation. WTO. (Available at: https://www.wto.org/english/tratop_e/tradfa_e/tradfa_e.htm)
27 Trade facilitation — Cutting "red tape" at the border. WTO. (Available at: https://www.wto.org/english/tratop_e/tradfa_e/tradfa_introduction_e.htm)
and semiconductors are the fifth-largest U.S. export, the U.S. directly benefits from the TFA's lowering of international barriers to trade.28

V. TBT AGREEMENT HAS PROMOTED GLOBAL INTEROPERABILITY

The Agreement on Technical Barriers to Trade (TBT) aims to ensure that technical regulations, standards, and conformity assessment procedures are non-discriminatory and do not create unnecessary obstacles to trade. This agreement has been incredibly important to the semiconductor industry, and to the information and communications technology (ICT) industry more broadly, in promoting global standards and interoperability. Interoperability is a basic requirement for the widespread, global adoption of U.S. technology products. There are many barriers to interoperability, including unique country standards, technology mandates, discriminatory regulations, and burdensome conformity assessment procedures. The TBT agreement has been critical in promoting global interoperability by prohibiting policies and technical measures that discriminate between foreign and domestic products or create "unnecessary" obstacles to trade, as well as by requiring the use of international standards over unique domestic standards as the basis for technical regulations, except in narrow circumstances. The TBT Agreement’s transparency provisions regarding consultation and notice also has helped create a more predictable trading environment. The TBT Agreement Code of Good Practice requires, among other things, notification to the public and to the WTO TBT Committee of any and all technical measures that may have a significant effect on trade, and a 60-day comment period and mandatory reply to all comments received by domestic and international stakeholders.

VI. WTO REFORMS AND MODERNIZED TRADE RULES WILL FURTHER ENHANCE BENEFITS OF GLOBAL TRADE

Despite its many benefits over the past 25 years, the WTO has many areas in need of reform and improvement to combat discriminatory and market-distorting practices in the ICT sector. Current global trade tensions underscore the importance of establishing more robust global fair-trade disciplines that protect and strengthen the semiconductor industry and the broader global economy. Examples of areas in which the semiconductor industry could benefit from stronger and modernized global trade rules can be found below. These reforms should be advocated on multiple fronts including the WTO, regional trade blocs, as well as bilateral agreements.

1. STRENGTHEN LEGAL PROTECTIONS TO ENHANCE TRADE SECRETS PROTECTION

Trade secrets are a critical and major asset for nearly all semiconductor companies. Yet despite their tremendous importance and the inclusion of certain protections in TRIPS, trade secrets remain extremely vulnerable, especially in jurisdictions with weak laws and/or enforcement practices. More problematic is the wholesale misappropriation of trade secrets enabled or encouraged as result of government industrial policy. Modern trade agreements should require criminal penalties for trade

secret theft, including theft by governments or theft by means of cyber intrusion, and strengthened procedures to protect trade secrets during conformity assessment procedures, such as banning forced disclosure of software source code or other sensitive IP in certification/regulatory schemes.

2. **STRENGTHEN SUBSIDY & SOE DISCIPLINES SO COMPANIES CAN COMPETE FAIRLY BASED ON MARKET CONSIDERATIONS**

The efforts by some governments to provide unprecedented levels of subsidization to develop their domestic semiconductor capabilities has the potential to seriously distort semiconductor markets and generate excess capacity and dumping. WTO disciplines on subsidies and state-owned enterprises (SOEs) should be strengthened to ensure that the competitiveness of companies and their products, not government intervention, is the principal driver of industry success. Key areas of subsidy reform should include:

1) Restoring the “dark amber” category for certain types of highly trade-distorting domestic subsidies that are deemed to cause “serious prejudice” under SCM Article 6;
2) Improving enforcement by establishing a presumption of serious prejudice for programs that governments fail to notify;
3) Addressing subsidies provided by and to SOEs by defining “public body” based on an objective control standard;
4) Expanding prohibited assistance (non-commercial assistance) to more effectively capture government assistance that creates excess capacity or leads to market displacement;\(^29\) and
5) Clarifying the provisions of the SCM Agreement in Footnote 13 regarding “threat” of serious prejudice to cover situations in which government subsidies are likely to cause future adverse effects or future injury to a targeted industry.

3. **PREVENT FORCED LOCALIZATION OF DIGITAL INFRASTRUCTURE & FORCED TECHNOLOGY TRANSFERS**

Governments are increasingly using “forced localization” tactics to unfairly advantage domestic companies and/or force foreign investors to use domestic technology, transfer their own technology, localize data storage and processing, or build expensive infrastructure in a region as a condition of market access. These rules raise costs, distort markets, reduce global interoperability, increase the risk of unauthorized disclosure or theft of IP, and represent a thinly disguised form of import substitution, which is already prohibited by WTO rules. Establishing new trade rules that prevent WTO members from requiring companies to build technology infrastructure in their market or requiring companies to purchase or use local technology will help ensure fair trade, data efficiency, cost efficiency, global interoperability and technology choice.

4. **ENSURE MARKET ACCESS FOR INNOVATIVE ENCRYPTION PRODUCTS**

With semiconductor-enabled encryption now used in nearly all commonly used and globally traded ICT products, the adoption of restrictive policies (i.e. import bans, technology mandates or requirements to transfer or provide access to proprietary information) would erect a major market access barrier and threaten the exportation of semiconductors and other ICT products on the scale of hundreds of billions

\(^{29}\) As reported by the WTO’s Working Party on the Ascension of China, China’s WTO ascension agreement requires any business “decisions by state-owned and state-invested enterprises had to be based on commercial considerations as provided in the WTO Agreement.” (Available at: https://www.wto.org/english/thewto_e/acc_e/wp_acc_china_e.doc, p. 9)
of dollars. WTO negotiators should agree on new rules that prevent countries from taking actions that block or place discriminatory restrictions on commercial foreign products with encryption, or that block companies from using the strongest available security technologies in the marketplace.

5. **ELIMINATE DUTIES ON SEMICONDUCTOR-RICH PRODUCTS, ELECTRONIC TRANSMISSIONS**

Reciprocal duty-free treatment for both tangible and intangible ICT goods has lowered consumer prices, facilitated trade, and spurred demand for ICT and digital products, to the benefit of goods and services exporters of all sizes. To further enhance these benefits and promote technology innovation and adoption, all WTO members should be encouraged to join both the ITA and ITA expansion and make permanent the WTO’s existing moratorium on the imposition of customs duties on trade in electronic transmissions.

VI. **CONCLUSION**

The steady opening of markets, leveling of the global playing field, and establishment of multilateral disciplines on IP, subsidies, and other government policies spearheaded by the General Agreement on Tariffs and Trade (GATT) and World Trade Organization (WTO) over the past 25 years have been critical to the success of the global semiconductor industry. Landmark agreements, such as TRIPS, ITA, ITA Expansion, SCM, and TFA, have also broken down protectionist barriers and allowed semiconductor companies to expand their businesses and reach new markets without fear of losing their hard-won innovations. Certainly, discriminatory and market-distorting practices persist in the ICT sector and beyond, and these practices need to be addressed. Governments can best tackle these challenges, however, by strengthening the WTO’s negotiating function and updating global trade rules to ensure relevance for modern trade issues. As one of the world’s most widely traded products, semiconductors can best continue powering the world’s economy and employing workers if we ensure the global marketplace is free, open, and fair to all.  

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30 Semiconductor Devices. OEC. (Available at: https://oec.world/en/profile/hs92/semiconductor-devices#:~:text=Semiconductor%20Devices%20are%20the%20world%20's,and%20Malaysia%20(%246.59B))